

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ronald J. Bolam et al. : Art Unit:
Serial No.: To Be Assigned : Examiner:
Filed: Herewith :
For: : SILICON-ON-INSULATOR CHIP :
HAVING AN ISOLATION BARRIER :
FOR RELIABILITY :

JC872 U.S. PTO
09/878681
06/11/01**CONTINUATION-IN-PART APPLICATION OF:**

Applicants: Ronald J. Bolam et al. : Art Unit:
Serial No.: 09/859,146 : Examiner:
Filed: May 16, 2001 :
For SILICON-ON-INSULATOR CHIP :
HAVING AN ISOLATION BARRIER :
FOR RELIABILITY (As Amended) :

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

S I R :

Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and to the duty of disclosure set forth in 37 C.F.R. § 1.56, the Examiner in charge of the above-identified application is requested to consider and make of record the references listed on the PTO 1449 (R&P) submitted herewith.

Although the information submitted herewith may be "material" to the Examiner's consideration of the subject application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.


In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

Under 37 C.F.R. § 1.98(d), copies of the references listed on the enclosed PTO Form 1449 are not required to be provided because they were cited by or submitted to the Patent and Trademark Office in prior Continuation application Serial No. 09/859,146, filed May 16, 2001, Divisional application Serial No. 09/148,918, filed September 4, 1998, and the parent application Serial No. 09/009,445, filed January 20, 1999 which has issued on October 17, 2000 (Pat. No. 6,133,610) which is relied upon for an earlier filing date under 35 U.S.C. § 120.

This Information Disclosure Statement is being filed together with the application. Therefore, no fee or certification is required. 37 C.F.R. § 1.97(b).

Respectfully submitted,

RATNER & PRESTIA


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DNC/img

Enclosure: PTO Form 1449

Dated: June 11, 2001

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The Assistant Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. 09-0458 (IBM Corporation) of any fees associated with this communication.

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Date of Deposit: June 11, 2001

I hereby certify that this paper and fee is being deposited, under 37 C.F.R. § 1.10 and with sufficient postage, using the "Express Mail Post Office to Addressee" service of the United States Postal Service on the date indicated above and that the deposit is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.


Kathleen Libby